

FIG. 2A

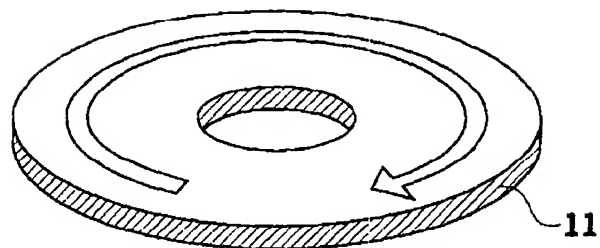


FIG. 2B

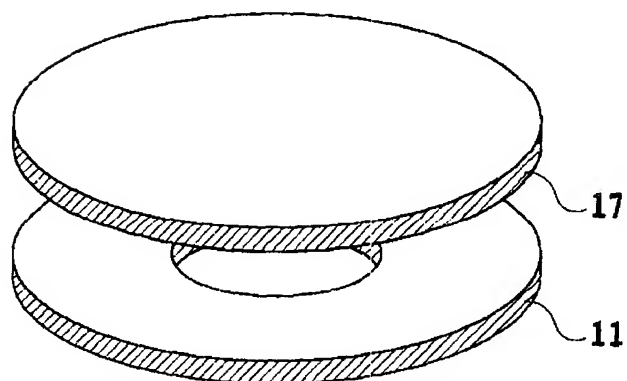


FIG. 2C

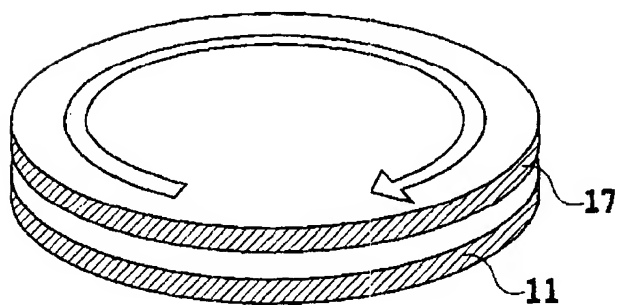


FIG. 3

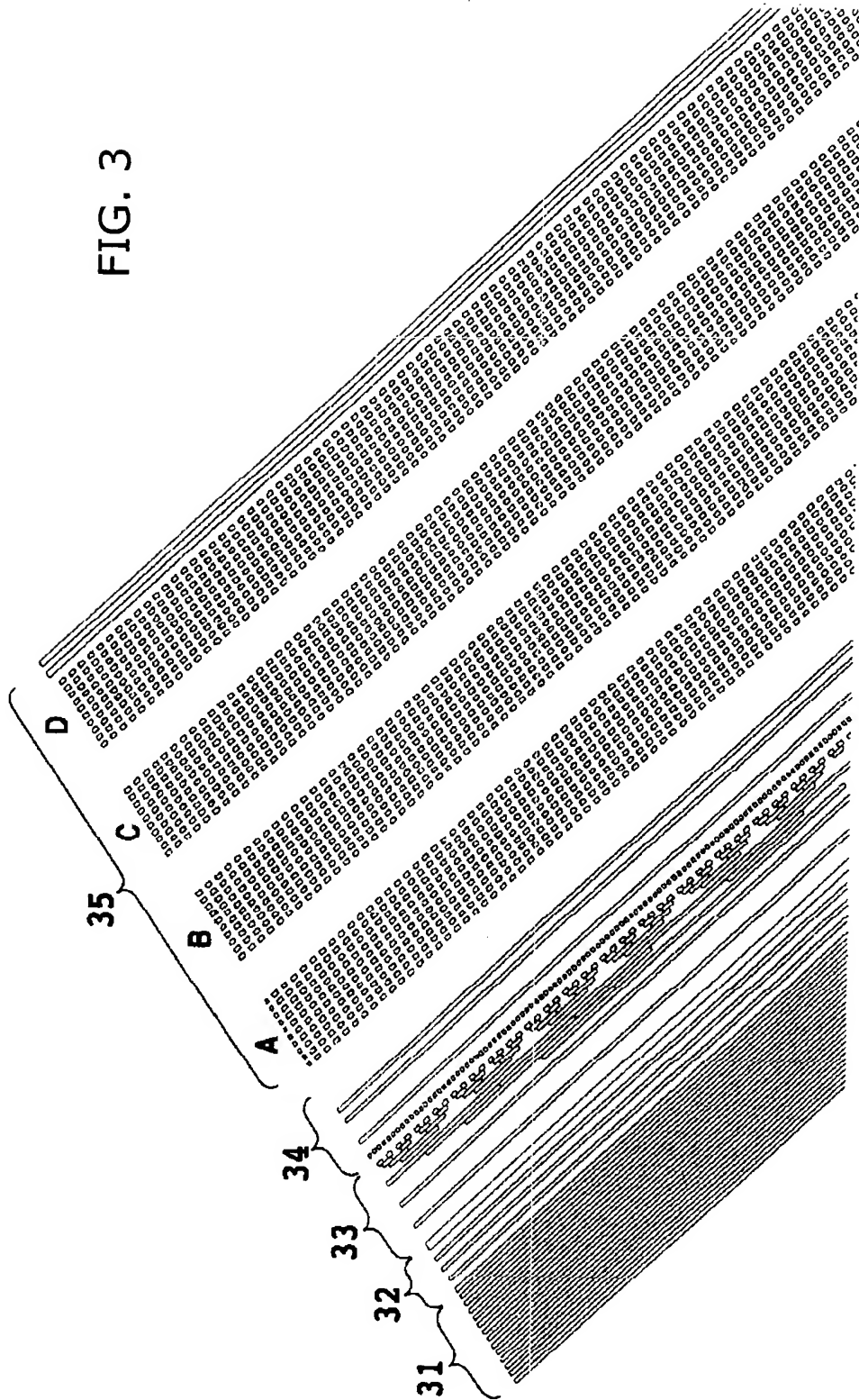


FIG. 4A

FIG. 4B

Data Before Moduation	Data After Modulation
01	X01
10	010
11	X011
0001	X00001
0010	X00000
0011	010001
0000	010000

X is 1 when the bit after modulation is 0

FIG. 5

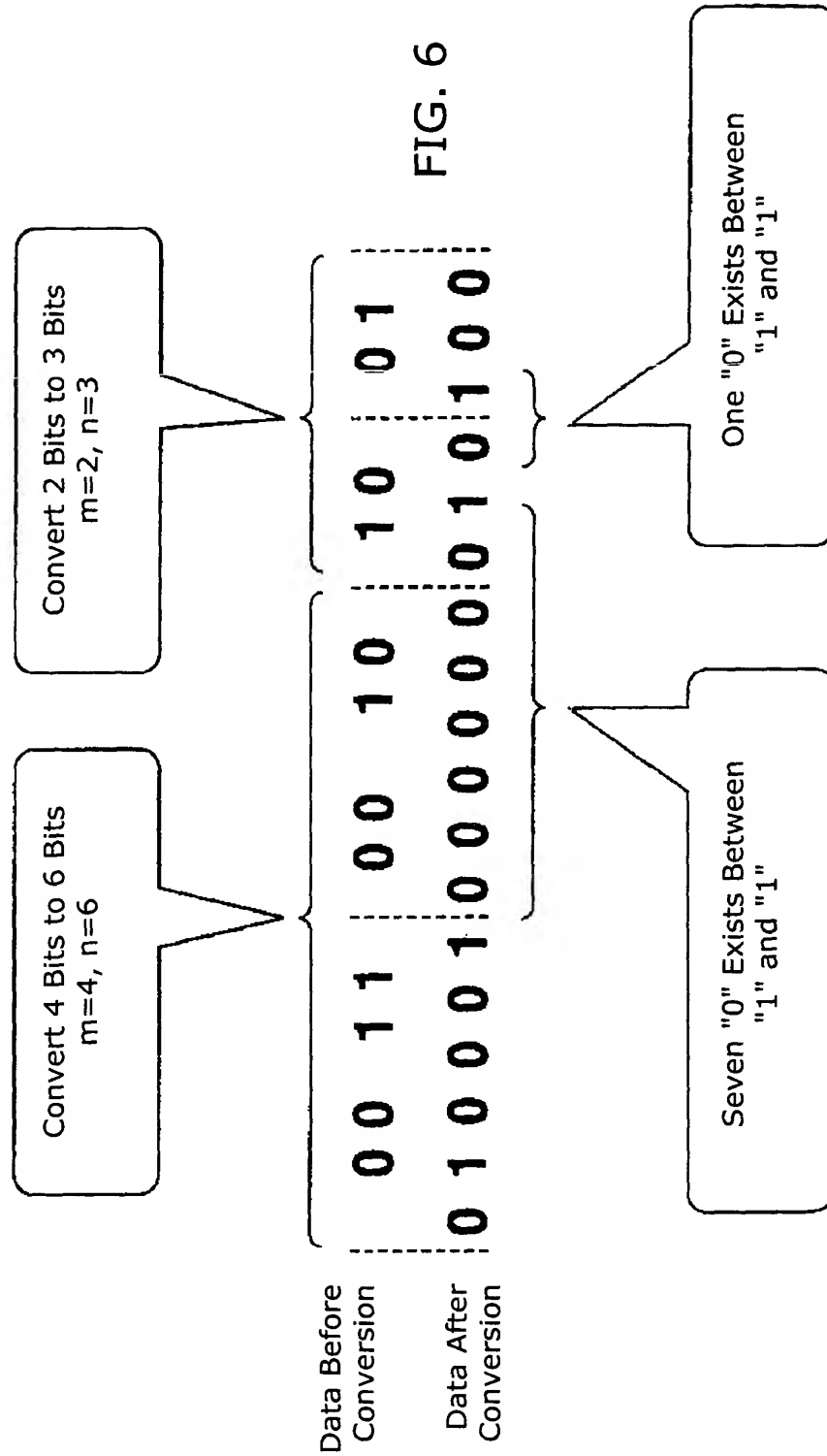


FIG. 7A

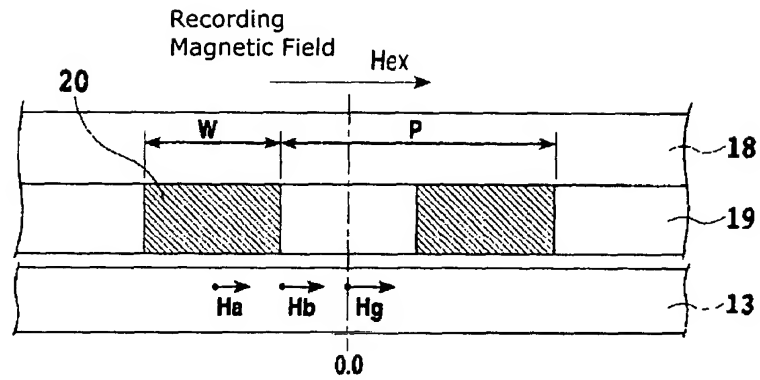


FIG. 7B

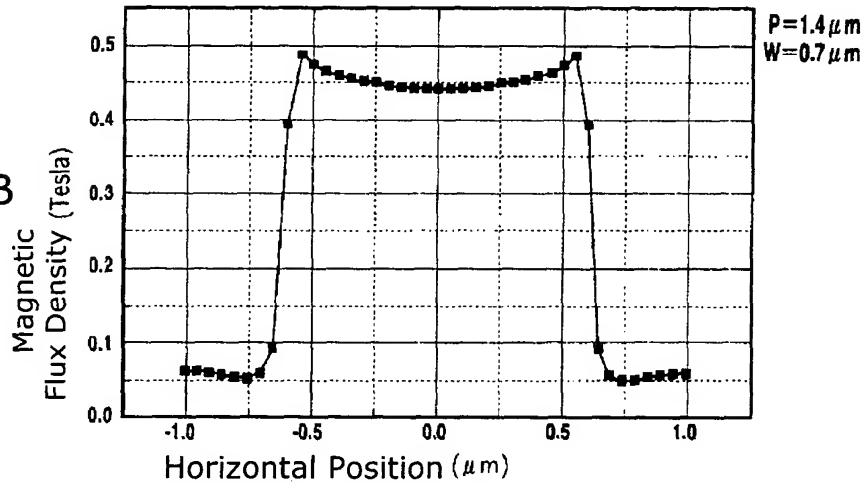


FIG. 7C

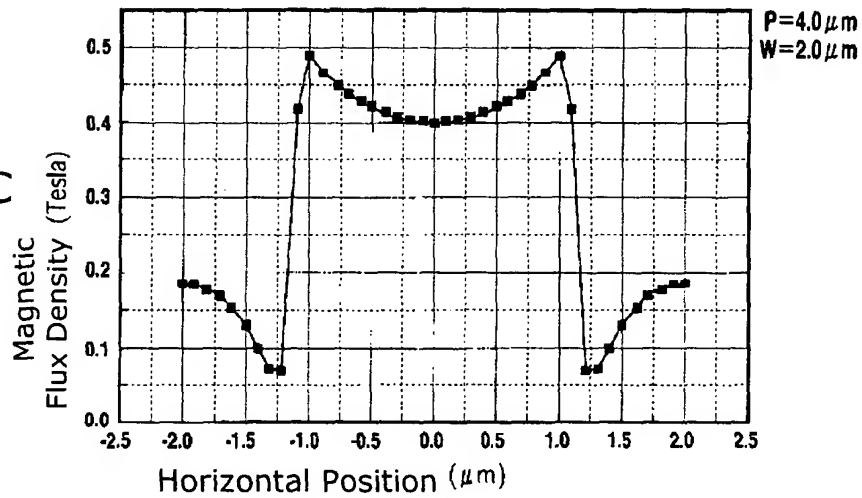


FIG. 8A

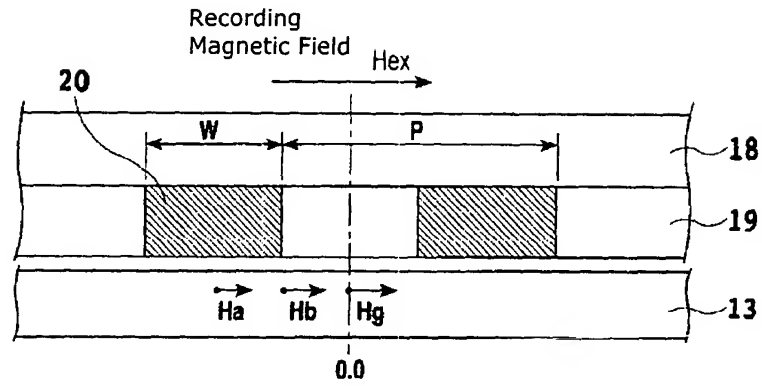


FIG. 8B

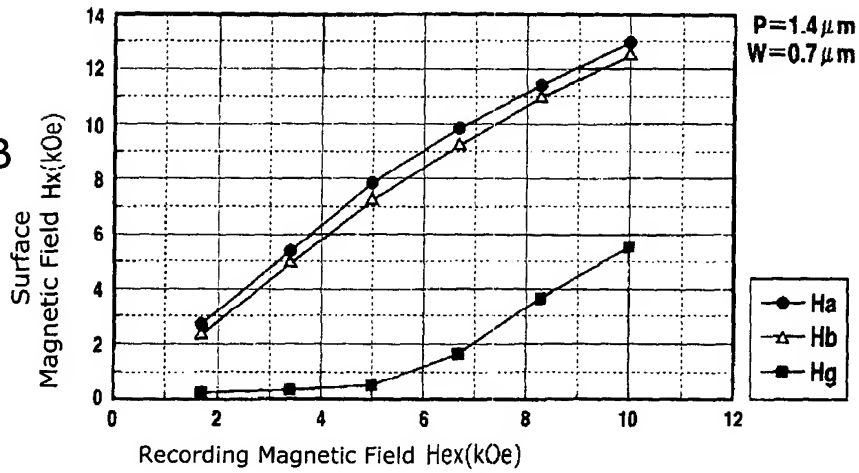


FIG. 8C

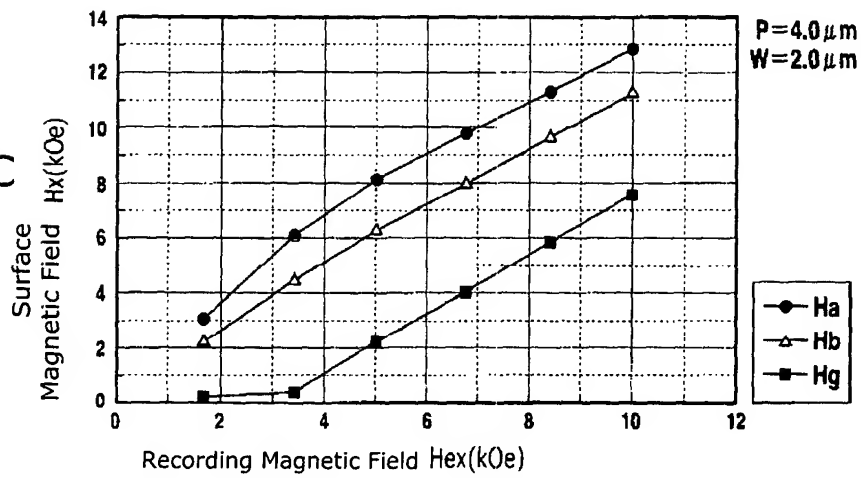
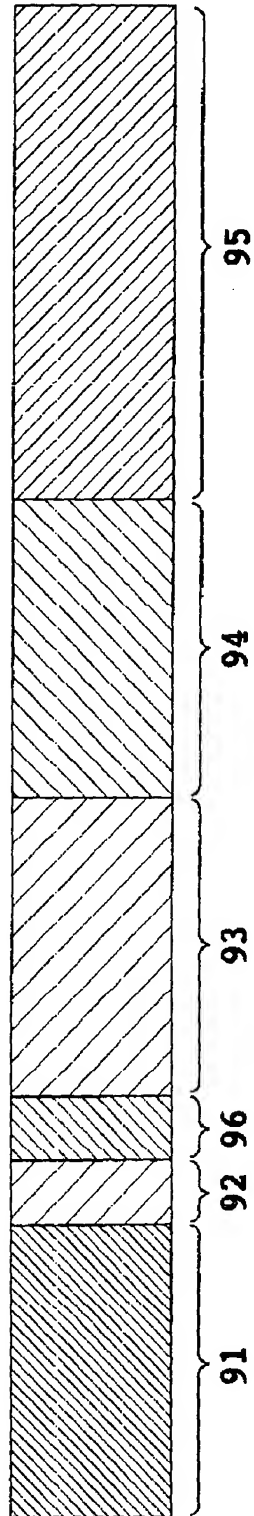


FIG. 9



CYLID	N
0	0 0 0 0 0 0 0 0 0 0 1 1 1 1 1 1 1 1 0 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 7
1	0 0
2	0 0
3	0 0
4	0 0
5	0 0
6	0 0
7	0 0
8	0 0
9	0 0
10	0 0
11	0 0
12	0 0
13	0 0
14	0 0
15	0 0
247301	1 0 0 0 1 0 0 1 0 1 0 0 0 0 0 1 1 1
247302	1 0 0 0 1 0 0 1 0 1 0 0 0 0 0 1 0 1
247303	1 0 0 0 1 0 0 1 0 1 0 0 0 0 0 1 0 0
247304	1 0 0 0 1 0 0 1 0 1 0 0 0 0 0 1 1 0 0
247305	1 0 0 0 1 0 0 1 0 1 0 0 0 0 0 1 1 0 1

CYL ID	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	Data Inversion Location
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14
	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14,14
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7,14,14
247301	1	0	0	0	1	0	0	1	0	1	0	0	0	0	0	1	1
247302	1	0	0	0	1	0	0	1	0	1	0	0	0	0	0	1	0
247303	1	0	0	0	1	0	0	1	0	1	0	0	0	0	0	1	0
247304	1	0	0	0	1	0	0	1	0	1	0	0	0	0	0	1	0
247305	1	0	0	0	1	0	0	1	0	1	0	0	0	0	0	1	0

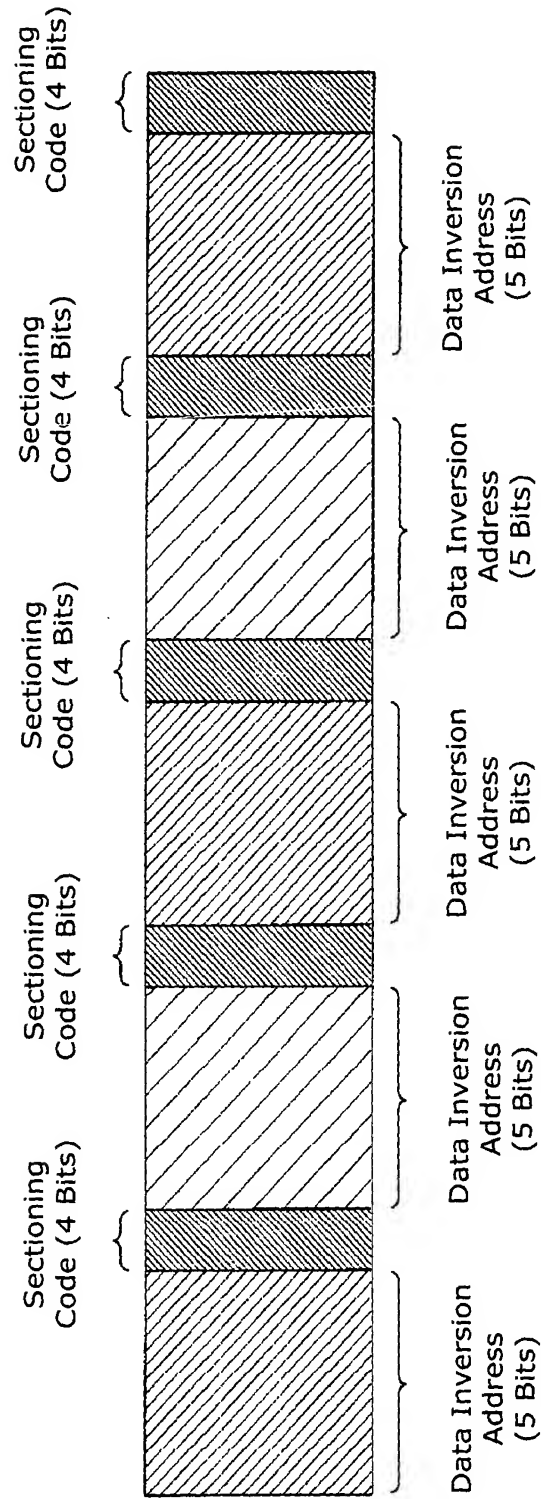


FIG. 11

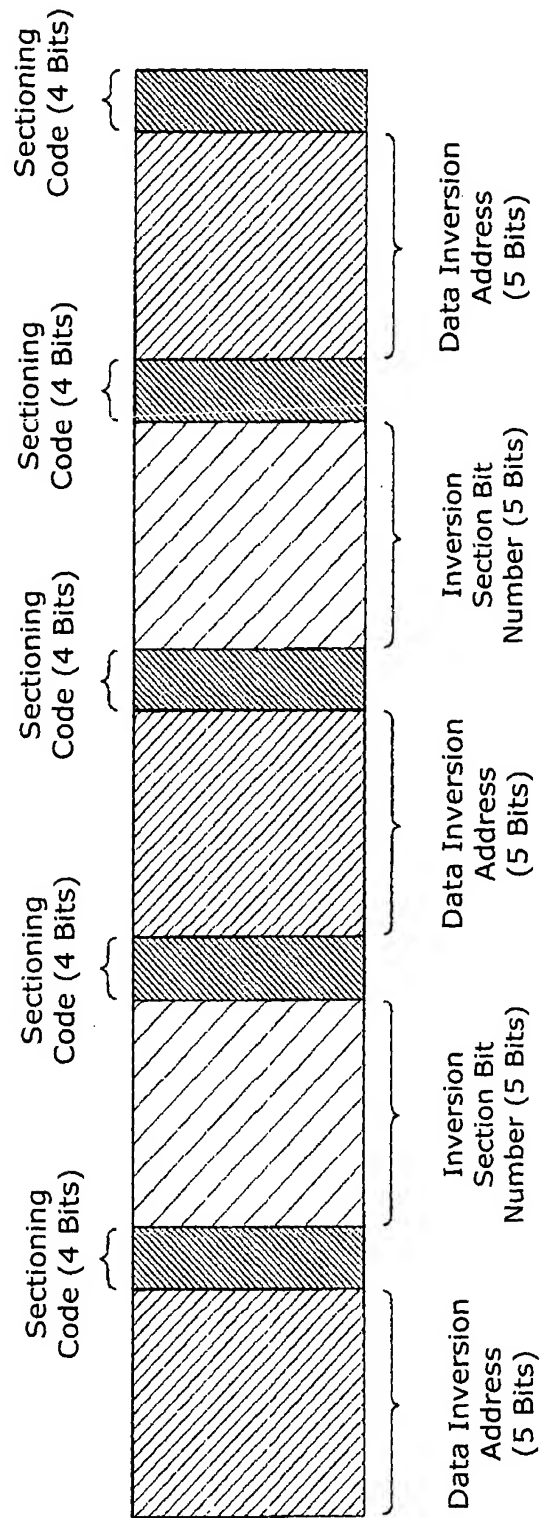


FIG. 12